LAN Transformer Module

LAP-16G241P1E8

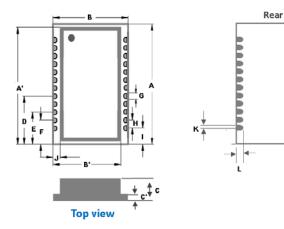
1. Features

- 1. Low profile, small footprint saves board space and height
- 2. Compliant with IEEE 802.3 standard for 1000BASE-T
- 3. Pin to Pin compatibility with LAN transformer
- 4. Operating temperature range: 0°C to +85°C
- 5. Storage temperature range: 0°C to +85°C
- 6. 100% Lead (Pb)-Free and RoHS compliant.

2. Applications

- 1. 1000 Base-T, Single Port, Low profile Modules (24 Pin)
- 2. Notebook pc LAN Transformer Module
- 3. Hub switch, Ap router Multi-port LAN Transformer.
- 4. At 802.3at (720mA)

3. Dimensions

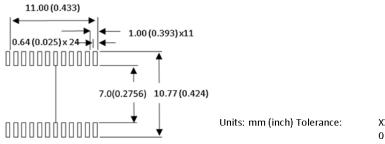


	A(mm)	A'(mm)	B(mm)	B'(mm)	C(mm)	C'(mm)
	16.5	16.0	10.3	9.65	4.1	0.8
Size	D(mm)	E(mm)	F(mm)	G(mm)	H(mm)	l(mm)
Size	6.75	4.75	3.75	0.4	1.0	2.75
	J(mm)	K(mm)	L(mm)			
	0.65	0.2	1.0			

Tolerance: XX.X0 +/-0.25(mm)

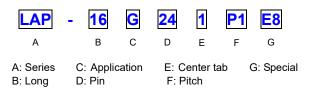
0.XX +/-0.05(mm)

Recommend PC Board Pattern



XX.X0 +/-0.25 (0.010) 0.XX +/-0.05 (0.002)

4. Part Numbering



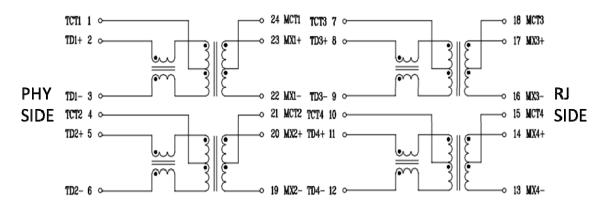
5. Specification

Part Number	Insertion Loss (dB Max)	Return Loss (dB min)					Cross talk (db min)	DCMR(dB Min)	
	1~100Mhz	1~30Mhz	40Mhz	50Mhz	60~80Mhz	100Mhz	1~100Mhz	1~60Mhz	60~100Mhz
LAP-16G241P1E8	-1.1	-18	-14.4	-13.1	-12	-10	-35	-35	-30

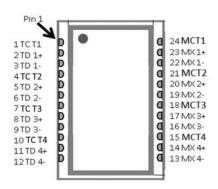
Note:

- 2. Hi-Pot resistance of 1500 VAC for 1 minute
- 3. Recommended the design modules should be assembled on the second side.

6. Schematic



7. Pin Define



8. Reliability and Test Condition

Item	Performance	Test Condition				
Insertion Loss						
Retuen Loss	Refer to standard electrical characteristics list.	Agilent E5071C				
Cross talk	1	,				
DCMR						
Operating Temperature	0°C∼+85°C (Including self - temperature rise)					
Storage Temperature	0°C∼+85°C (Product without taping)					
Life Test	Appearance : No damage.	Preconditioning: Run through IR reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Temperature: 85±2°C Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs Preconditioning: Run through IR reflow for 3 times.				
Humidity Resistance Test	Insertion Loss: within spec. Return Loss: within spec.	(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Humidity: 85±2% R.H, Temperature: 85℃±2℃ Duration: 1000hrs Min. Bead: with 100% rated current. Inductance: with 10% rated current. Measured at room temperature after placing for 24±2 hrs				
Thermal shock Test	Appearance : No damage. Insertion Loss : within spec.	Preconditioning: Run through IR reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Step1: 0±2℃ 30±5min Step2: 25±2℃ ≦0.5min Step3: 85±2℃ 30±5min Number of cycles: 500 Measured at room temperature after placing for 24±2 hrs				
Vibration Test	Return Loss: within spec.	Preconditioning: Run through IR reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude:10g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations)				
Solderability Test	More than 95% of bottom terminal electrode should be covered with solder.	a. Method B, 4 hrs @155°C dry heat @235°C±5°C Test time:5 +0/-0.5 seconds. b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C Test time: 30 +0/-0.5 seconds.				
		Depth: completely cover bottom the termination				
Resistance To Solder Heat Test	Appearance : No damage.	Temperature(°C) Time(s) Temperature ramp/immersion and emersion rate Number of heat cycles				
		260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s 1				
		With the component mounted on a PCB with the device to be tested, apply a force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.				
Terminal Strength Test	Series No. 2(Kg) LAN 1.0(min.)	PCB				

9. Soldering and Mounting

9-1. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

9-1.1 IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

9-1.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

- Preheat circuit and products to 150℃ • Use a 20 watt soldering iron with tip diameter of 1.0mm
- 350°C tip temperature (max) • 1.0mm tip diameter (max) • Limit soldering time to 4~5 sec.

Fig.1 IR Soldering Reflow

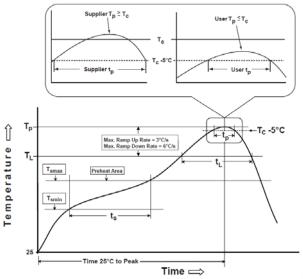
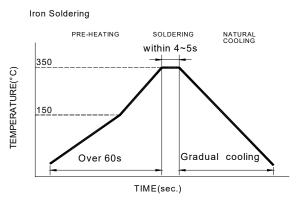


Fig.2 Iron soldering temperature profiles



Iron Soldering times: 1 times max

Reflow times: 3 times max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min(T _{smin}) -Temperature Max(T _{smax}) -Time(t _s)from(T _{smin} to T _{smax})	150°C 200°C 60-120seconds
Ramp-up rate(T_L to T_p)	3°ℂ/second max.
$\label{eq:Liquidus} \begin{array}{c} \text{Liquidus temperature}(T_L) \\ \text{Time}(t_L) \text{maintained above } T_L \end{array}$	217°C 60-150 seconds
Classification temperature(T _c)	See Table (1.2)
$\label{eq:top} \mbox{Time}(t_p) \mbox{ at Tc-} 5^{\circ}\mbox{\mathbb{C}} (\mbox{Tp should be equal to or less than Tc.)}$	< 30 seconds
Ramp-down rate(T _p to T _L)	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, **Tc**: the classification temperature.

For user (customer) Tp should be equal to or less than Tc.

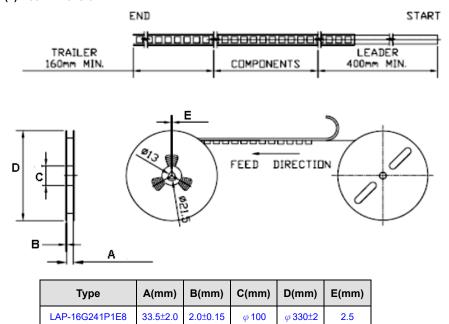
Table (1.2) Package Thickness/Volume and Classification Temperature (Tc)

	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000	
	<1.6mm	260℃	260℃	260 ℃	
PB-Free Assembly	1.6-2.5mm	260℃	250℃	245 ℃	
	≥2.5mm	250℃	245℃	245 ℃	

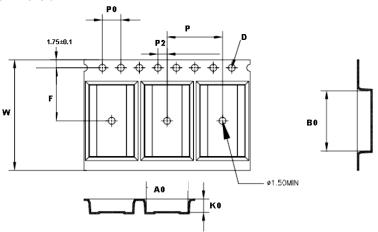
Reflow is referred to standard IPC/JEDEC J-STD-020E ∘

10. Packaging Information

(1) Reel Dimension



(2) Tape Dimension



Series	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	Po(mm)	P2(mm)	W(mm)	F(mm)	D(mm)
LAP-16G241P1E8	16.80±0.1	10.60±0.1	4.90±0.1	16.00±0.1	4.0±0.1	2.0±0.1	32±0.3	14.2±0.1	1.5±0.1

(3) Packaging Quantity

LAN	LAP-16G241P1E8
Chip / Reel	800

Application Notice

- Storage Conditions(component level)
- To maintain the solderability of terminal electrodes:
- 1. products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- $3. \ \mbox{Recommended}$ products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.