LAP-17E241P7B8

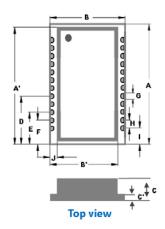
#### 1. Features

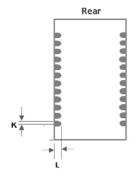
- 1. Low profile, small footprint saves board space and height
- 2. Design for 2.5G ethernet application
- 3. Pin to Pin compatibility with LAN transformer
- 4. Operating temperature range:  $-40^{\circ}$ C to  $+85^{\circ}$ C.
- 5. Storage temperature range:  $-40^{\circ}$ C to  $+85^{\circ}$ C.
- 6. 100% Lead (Pb)-Free and RoHS compliant.

## 2. Applications

- 1. 2.5G Base-T, Single Port, Low profile Modules w/Surge Protection (24 Pin)
- 2. POE applications with 900mA current capability.

#### 3. Dimensions



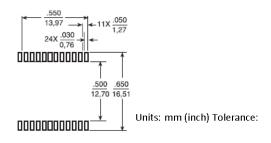


	A(mm)	A'(mm)	B(mm)	B'(mm)	C(mm)	C'(mm)
	17.53	17.03	14.6	13.92	4.5 max	0.8
Size	D(mm)	E(mm)	F(mm)	G(mm)	H(mm)	l(mm)
SIZE	6.86	4.32	3.05	0.4	1.27	1.78
	J(mm)	K(mm)	L(mm)			
	0.67	0.2	1.1			

Tolerance: XX.X0 +/-0.25(mm)

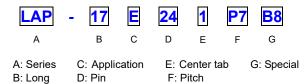
0.XX +/-0.05(mm)

#### **Recommend PC Board Pattern**



XX.X0 +/-0.25 (0.010) 0.XX +/-0.05 (0.002)

#### 4. Part Numbering



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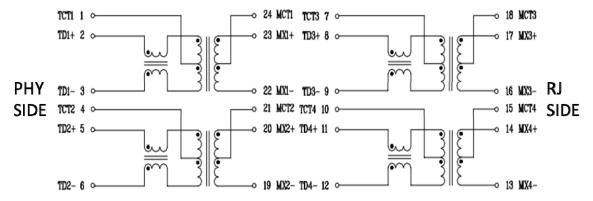
## 5. Specification

	OCL (uH Min)		on Loss Max)	Return L	oss (dB min)	Cross talk (dB min)	DCMR(dB Min)		
Part Number	@100KHz/0. 1V with 8mA DC Bias		100 <f≦ 250MHz</f≦ 	1≦f≦40MHz	40≦f≦250MHz	1≦f≦250MHz	1~ 50MHz	50~150MHz	150~250MHz
LAP-17E241P7B8	120	-1.0	-2.0	-16	-(16-10log <sub>10</sub> (f/40))	-30	-30	-20	-15

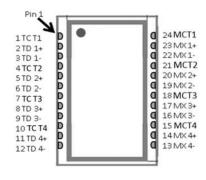
Note:

- 2. Hi-Pot resistance of 1500 VAC for 1 minute
- 3. Recommended the design modules should be assembled on the second side.

#### 6. Schematic



### 7. Pin Define



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# 8. Reliability and Test Condition

Item		Perfor	mance		Test Condition						
Insertion Loss Retuen Loss Cross talk DCMR	Refer to	standard electrical cha	aracteristics lis	t.	Agilent E5071C						
Operating Temperature	-40°C~+		mperature rise	·)							
Storage Temperature	-40°C~+	85°C (Product without ta	aping)	<u> </u>							
Life Test		- \			Preconditioning:Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles  Temperature: 85±2°C  Duration: 1000±12hrs						
Humidity Resistance Test	Insertion	nce: No damage. Loss: within spec. .oss: within spec.			Measured at room temperature after placing for 24±2 hrs  Preconditioning:Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow  Profiles  Humidity: 85±2% R.H,  Temperature: 85°C±2°C  Duration: 1000hrs Min.  Measured at room temperature after placing for 24±2						
Thermal shock Test	Insertion	ince : No damage. I Loss : within spec. .oss : within spec.			hrs  Preconditioning:Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles  Step1: -40±2℃ 30±5min  Step2: 25±2℃ ≤0.5min  Step3: 85±2℃ 30±5min  Number of cycles: 500 Measured at room temperature after placing for 24±2 hrs						
Vibration Test					Preconditioning:Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude:10g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations) •						
Solderability Test		an 95% of bottom termi with solder.	inal electrode	should be	Preheat: 150°C,60sec. • Solder:Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C • Flux for lead free: Rosin. 9.5% • Dip time: 4±1sec • Depth: completely cover the termination						
Resistance To Solder Heat Test	Annearan	ce:No damage.			Temperature (°C)	Time (s)	Temperature ramp/immersion and emersion rate	Number o heat cycles			
Resistance to Solder Real lest	Арреагап	ce - No damage.			260 ±5 (solder temp)         10 ±1         25mm/s ±6 mm/s         1           Depth: completely cover bottom the termination						
					With the component mounted on a PCB with the device to be tested, apply a force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.						
Terminal Strength Test		Series No.	2(Kg) 1.0(min.)		PCB		,				

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## 9. Soldering and Mounting

#### 9-1. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

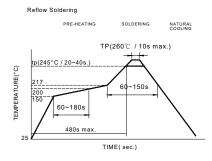
#### 9-1.1 Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

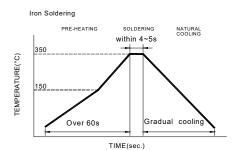
#### 9-1.2 Soldering Iron(Figure 2):

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150℃
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 350°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5 sec.



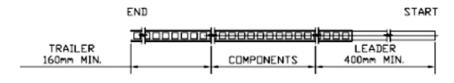
Reflow times: 3 times max Fig.1

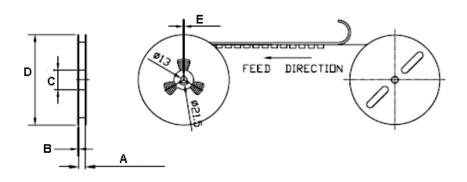


Iron Soldering times: 1 times max Fig.2

## 10. Packaging Information

#### (1) Reel Dimension

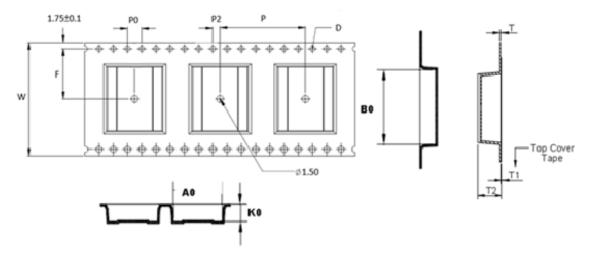




Туре	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
LAP-17E241P7B8	33.5±2.0	2.0±0.15	<i>φ</i> 100	φ 330±2	2.5

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#### (2) Tape Dimension



Series	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	Po(mm)	P2(mm)	W(mm)	F(mm)	D(mm)	T(mm)	T1(mm)	T2(mm)
LAP-17E241P7B8	17.93±0.1	15.3±0.1	4.80±0.1	24.0±0.1	4.0±0.1	2.0±0.1	32±0.3	14.2±0.1	1.5±0.1	0.4±0.05	0.06±0.01	5.6±0.1

#### (3) Packaging Quantity

LAN	LAP-17E241P7B8
Chip / Reel	400

#### **Application Notice**

- Storage Conditions(component level)
- To maintain the solderability of terminal electrodes:
- 1. products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40  $^{\circ}\mathrm{C}$  and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- $1.\ \dot{Products}\ should\ be\ handled\ with\ care\ to\ avoid\ damage\ or\ contamination\ from\ perspiration\ and\ skin\ oils.$
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.