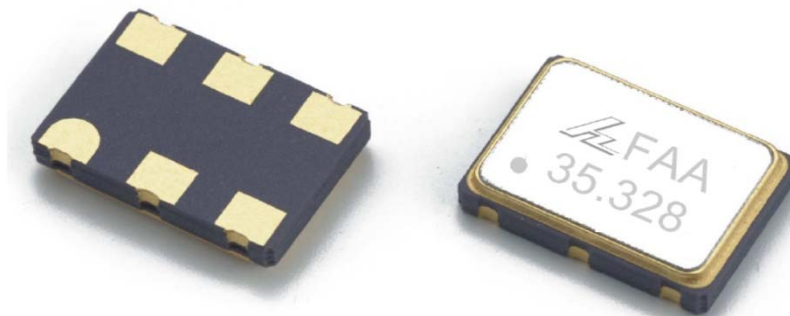


PECL Oscillator (7.0x5.0) SMD Type



Application of Oscillator

- ▶ PLL circuit
- ▶ Broad casting equipment
- ▶ AFC circuit
- ▶ Sonet/SDH.ATM
- ▶ FM modulator



Features

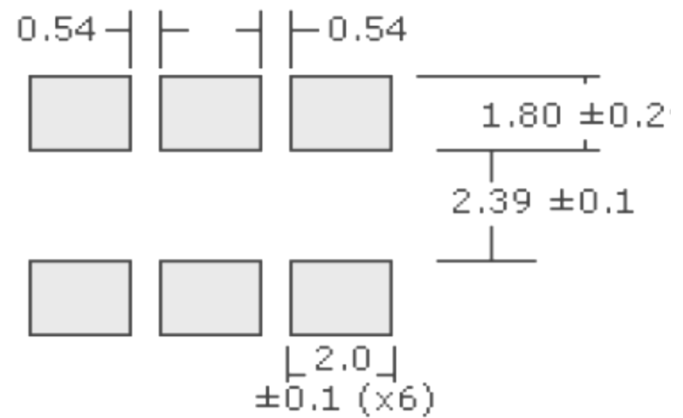
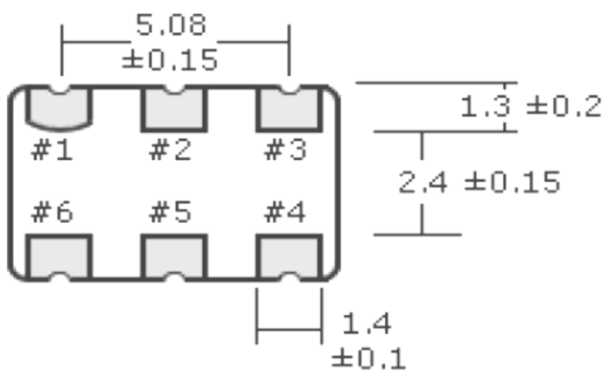
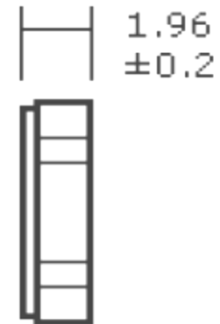
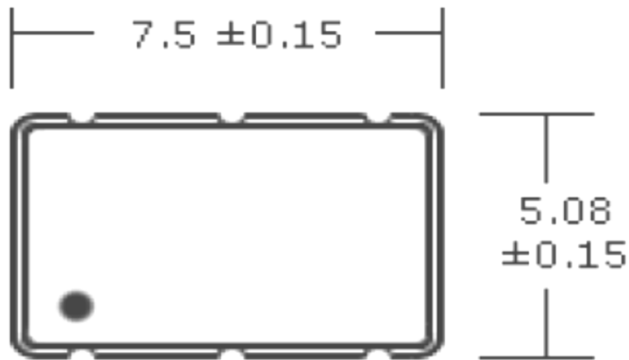
- ▶ High Frequency
- ▶ PECL Square Wave
- ▶ Ultra-Low Jitter
- ▶ Extended Operating Temperature
- ▶ SMD Version 6 Pad
- ▶ Rohs compliant / Pb free



Electrical Specification

PARAMETERS	CONDITIONS	CHARACTERISTICS	UNITS
Output Logic	-	PECL Square Wave	-
Input Voltage (VDD)	-	3.3 ±5%	VDC
Frequency Range (fO)	-	160.000 ~ 622.080	MHz
Operating Temperature (TOPR)	-	0 ~ +85	°C
Storage Temperature (TSTG)	-	-55 ~ +125	°C
	(a) - (e)	±50 max.	PPM
	(a) Frequency Tolerance	Inclusive of Overall Stability	-
	(b) Temperature Stability	Inclusive of Overall Stability (Operating Temperature)	-
	(c) Input Voltage Stability	Inclusive of Overall Stability (VDD ± 5%)	-
	(d) Load Stability	Inclusive of Overall Stability (RL ±	-
Overall Frequency Stability	(e) Aging (@ 25°C)	±3 max.	PPM/Y
Current Consumption	-	75 max.	mA
Rise Time (Tr)	-	400 max. (20% to 80% of Waveform)	ps
Fall Time (Tf)	-	400 max. (20% to 80% of Waveform)	ps
Output Voltage (Vol)	-	Vcc - 1.81V ~ Vcc - 1.02V	-
Output Voltage (Voh)	-	Vcc - 1.025V ~ Vcc - 0.88V	-
Load	-	50	Ohms
Duty Cycle	-	50 ±5	%
Start-Up Time (TS)	-	10 max.	mS
Phase Jitter	(12KHz to 20MHz)	RMS: 1 max.	ps

Dimensions (mm):



PIN CONNECTIONS	
#1	Tristate Enable High or No Connection (TS1)
#2	Tristate Enable High or No Connection (TS)
#3	Ground
#4	Output
#5	Complimentary Output
#6	Vdd